

# IEEE International Conference on Intelligent Systems and Embedded Design (ISED2023), DIT University, Dehradun, Uttarakhand

**Dates: December 15-17, 2023**

**Location: [DIT University, Dehradun]**

Day-1 (15 December, 2023)	Venue			Session Chair(s)
		Inauguration of the conference		
10:00-10:05	Auditorium, Vedanta Building	Welcome & Lamp Lighting		
10:05-10:10		Welcome of Dignitaries		
10:10-10:15		Address by the Chancellor, DITU		
10:15-10:25		Address by the VC, DITU		
10:25-10:35		Address & Summary about the Conference by the PVC, DITU		
10:35-10:40		Information Sharing by the Industrial Speaker		
10:40-10:45		Vote of Thanks by the General Chair - ISED2023		
10:45-11:30		Keynote Speech-1: PCI-Express: Continued Journey of an Open Innovation Slot in all Computing Platforms Spanning Decades by Dr. Debendra Das Sharma, Intel Senior Fellow and co-GM Memory and I/O Technologies, Data Platforms and AI Group, Intel Corporation		
11:30-12:15	Vedanta - Reception	High Tea		
12:15-13:00	Auditorium, Vedanta Building	Tutorial Session -I: Embedded Systems Security by Mr. Nagaraju N Kodalapura, Lead Offensive Security Researcher, IPAS, Intel Technology India Private Limited		
13:00-14:00	Vedanta - Reception	Lunch		
14:00-14:45	Seminar Hall - VISH101	Keynote Speech-2: Title of Talk: SOC Testing and Verification: Machine Learning Approaches Dr. Alman Chakrabarti, Visiting Professor & Director, A. K. Choudhary School of Calcutta		
14:45-15:00	VISHVESHWARAIYA LOBBY	High Tea		
15:00-17:15		<b>Parallel Sessions</b>	<b>Paper IDs</b>	
	VISH201	1: Embedded System Design Methodology & Tools (ESDMT)	50, 57, 71, 86, 115, 118, 121, 154, 162	Dr. Brajlata Chauhan, DIT University
	VISH301	4: Emerging Embedded Applications & Interdisciplinary (EEAI)	10, 11, 29, 40, 67, 80, 98, 124	Dr. Gaurav Dhiman, DIT University
	VISH305	8: Applications of Intelligent Systems (AIS)	14, 24, 34, 47, 54, 76, 79	Dr. Geetanjali Rathee, NSUT, Delhi
Day-2 (16 December, 2023)	Venue			
10:00-10:45	Seminar Hall - VISH101	Keynote Speech-3 : Recommendation systems: Challenges and solutions by Dr. Rajeev Rastogi, Vice President of Machine Learning, Amazon's International Stores Business		
10:45-11:15	Seminar Hall - VISH101	Inougration of the UCOST sponsored Workshop on Intellectual Property Rights Awareness		
11:15-11:30	VISHVESHWARAIYA LOBBY	High Tea		
11:30-13:00		<b>Parallel Sessions</b>	<b>Paper IDs</b>	
	VISH201	5: Artificial Intelligence of Things (AIoT)	22, 23, 38, 41, 43, 74, 100	Dr. Brajlata Chauhan, DIT University
	VISH301	7: Intelligent Systems and Methodology (ASM)	8, 15, 20, 27, 42, 44, 60	Dr. Gaurav Dhiman, DIT University
		8: Applications of Intelligent Systems (AIS)	82, 94, 95, 97, 101, 102, 106	
13:00-14:00	Vedanta Reception	Lunch		
14:00-15:15		<b>Parallel Sessions</b>	<b>Paper IDs</b>	
	VISH201	5: Artificial Intelligence of Things (AIoT)	113, 130, 135, 152, 155, 180	Dr. Pradeep Singh Rawat, DIT University
	VISH301	7: Intelligent Systems and Methodology (ASM)	64, 68, 78, 99, 103	Dr. Garima Verma, DIT University
	VISH305	2: Domain-Specific, and Reconfigurable Chips (DSRC)	51, 73, 85, 170, 172	Dr. Srabanti Maji, DIT University
15:15-15:30	VISHVESHWARAIYA LOBBY	High Tea		
15:30-17:00		<b>Parallel Sessions</b>	<b>Paper IDs</b>	
	VISH201	5: Artificial Intelligence of Things (AIoT)	156, 158, 160, 165	Dr. Ravindra Sharma, SRHU
	VISH301	7: Intelligent Systems and Methodology (ASM)	117, 146, 150, 157, 159, 176	Dr. Geetanjali Rathee, NSUT, Delhi
	VISH305	8: Applications of Intelligent Systems (AIS)	107, 116, 120, 125, 134, 137, 46	Dr. Sunil Ghildiyal, DIT University
19:00-20:30	Vedanta Reception	Conference Dinner		
Day-3 (17 December, 2023)	Venue			
10:00-10:45	Seminar Hall - VISH101	Tutorial Session-2: Real Time Operating Systems		
10:45-11:15	VISHVESHWARAIYA LOBY	High Tea		
11:15-13:00		<b>Parallel Sessions</b>	<b>Paper IDs</b>	
	VISH201	4: Emerging Embedded Applications & Interdisciplinary (EEAI)	132, 148, 164, 181, 182, 183, 184	Dr. Pradeep Singh Rawat, DIT University
	VISH301	3: Secure Mobile & Embedded Devices (SMED)	114, 136, 151, 70, 108, 144, 145	Dr. Garima Verma, DIT University
	VISH305	8: Applications of Intelligent Systems (AIS)	140, 141, 143, 153, 173, 178, 179	Dr. Srabanti Maji, DIT University
13:00-14:00	Vedanta Reception	Lunch		
14:00-15:30	VISH301	Doctoral Symposium		

15:30-15:45	VISHVESHWARAIYA LOBBY	High Tea		
15:45-16:15	Seminar Hall - VISH301	Valedictory Function		
<b>Paper Presentation time including Q/A session is 15 - 10 minutes.</b>				
<b>* Rooms are in Vishveshwaraiya Building(Seminar Hall VISH101, VISH201, VISH301, VISH305)</b>				
<b>** Lunch venue will be Vedanta Reception for all three days</b>				

<b>UCOST sponsored Workshop on Intellectual Property Rights Awareness</b>			
<b>DIT University, Dehradun, Uttarakhand</b>			
<b>Date: December 17, 2023</b>			
<b>Tentative Workshop Schedule</b>			
Date	Venue	Activity	
10:00 AM-10:45 AM	VISHVESHWARAIYA 4th Floor	Offline registration	
10:45 AM-11:15 AM	Seminar Hall- VISH101	Inauguration	
11:15 AM-11:30 AM	LOBBY	High Tea	
11:30 AM- 12:15 PM	Seminar Hall- VISH101	Keynote Session-01	
12:15 PM- 1:00 PM	Seminar Hall- VISH101	Keynote Session-02	
1:00 PM- 2:00 PM	Vedanta Reception	Lunch	
2:00 PM- 2:45 PM	Seminar Hall- VISH101	Keynote Session-03	
2:45 PM- 3:15 PM	Seminar Hall- VISH101	Idea Exchange Session	
3:15 PM- 3:30 PM	VISHVESHWARAIYA LOBBY	High Tea	
3:30 PM- 4:00 PM	Seminar Hall- VISH101	Quiz	
4:00 PM- 4:30 PM	Seminar Hall- VISH101	Valedictory Function	

<b>Doctoral Symposium</b>		
Date & Time	Venue	Name and Affiliation
December 17, 2023:14:00 - 14:15	VISH301	Anurag Shrivastava, DIT University, Dehradun
December 17, 2023:14:15 - 14:30	VISH301	Pooja Gupta, DIT University, Dehradun
December 17, 2023:14:30 - 14:45	VISH301	Ankit Agarwal, DIT University, Dehradun
December 17, 2023:14:45 - 15:00	VISH301	Tarun Kumar, DIT University, Dehradun
December 17, 2023:15:00 - 15:15	VISH301	Ena Jain, DIT University, Dehradun
December 17, 2023:15:15 - 15:30	VISH301	Monika Srivastava, DIT University, Dehradun
December 17, 2023:15:30 - 15:45	VISH301	Meghavi Rana, DIT University, Dehradun